

<b>INFORMATION DISCLOSURE STATEMENT PTO-1449</b>	Att. Docket No.: 042138	Serial No.: <del>10/380,468</del> 10/782,770
	Applicants: Hiroe KAWAMURA	
	Filing Date: February 23, 2004	Group Art Unit: <del>3644</del> 2891


### U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					

### FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Translation (Yes or No)	
<u>1A</u>	AC	09-051097 ~	02/18/97	Japan	Abstract & Discussed in the Spec.
_____	AD	2002-091342 ~	03/27/02	Japan	Abstract & Discussed in the Spec.
_____	AE	10-326790 ~	12/08/98	Japan	Abstract & Discussed in the Spec.
_____	AF	10-335659 ~	12/18/98	Japan	Abstract & Discussed in the Spec.
_____	AG	08-264780 ~	10/11/96	Japan	Abstract & Discussed in the Spec.
_____	AH	08-264401 ~	10/11/96	Japan	Abstract & Discussed in the Spec.
_____	AI	08-264402 ~	10/11/96	Japan	Abstract & Discussed in the Spec.
_____	AJ	08-321443 ~	12/03/96	Japan	Abstract & Discussed in the Spec.
_____	AK	05-347256 ~	12/27/93	Japan	Abstract & Discussed in the Spec.
<u>1A</u>	AL	2002-003295 ~	01/09/02	Japan	Abstract & Discussed in the Spec.
_____	AM				

### OTHER DOCUMENTS

<u>1A</u>	AN	H. Sayama et al., "Effect of <100> Channel Direction for High Performance SCE Immune pMOSFET with Less Than 0.15μm Gate Length", IEEE, 1999
_____	AO	
Examiner		
Date Considered	4/25/05	

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Application Number	10/782,770
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First Named Inventor	Hiroe KAWAMURA
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Examiner Name	Unknown
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Date Considered	4/30/05
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